

## PATENT APPLICATION Do. No. 8371-127

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Apostolos VOUTSAS and Yukihiko NAKATA

Serial No.

09/945,063

Examiner: George R. Fourson III

Filed:

August 31, 2001

Group Art Unit: 2823

For:

METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM TRANSISTOR (TFT) AND THE DEVICE FORMED THEREBY

Box Non-Fee Amendment Assistant Commissioner for Patents Washington, D.C. 20231

TRANSMITTAL LETTER

Responsive to the Office Action dated October 31, 2002, enclosed is an amediment in over-identified application. the above-identified application.

Any deficiency or overpayment should be charged or credited to deposit account [X] number 13-1703.

Respectfully submitted,

Hosoon Lee

Limited Recognition Under 37 CFR § 10.9(b)

MARGER, JOHNSON & McCOLLOM, P.C. 1030 S.W. Morrison Street Portland, Oregon 97205 (503) 222-3613

I NUMBRY CENTRY THAT THIS COR-COPONDENCE IS BEING DEPOSITED THE UNITED STATES POSTAL TAMICE AS FIRST CLASS MAIL IN AN AVELOPE ADDRESSED TO: 1 T COMMISSIONER OF PAYENTS AND

ASSISTANT COMMISSIONER FOR -ATENTS, WASHINGTON D.C. 20331 TOADEMARKS, 2904 CRYSTAL DRIVE FAILBIGTON, VA 22262-3513